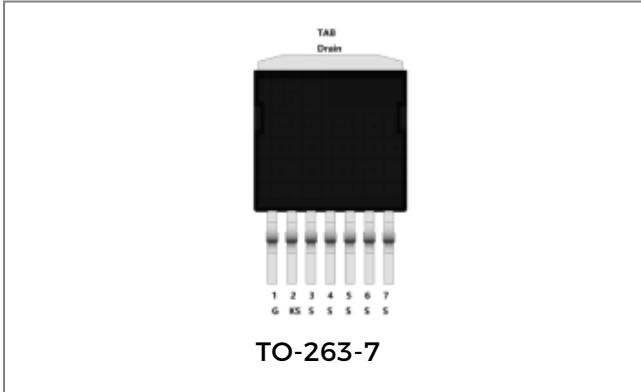


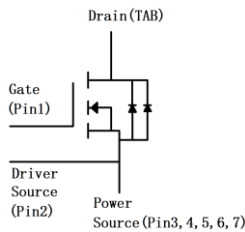
S3S0080120J SIC POWER MOSFET



Features

- Positive temperature characteristics, easy to parallel.
- Low on-resistance typ. $R_{DS(on)} = 80 \text{ m}\Omega$.
- Fast switching speed and low switching losses.
- Very fast and robust intrinsic body diode.
- Process of non-bright tin electroplatin.
- "-A" is an AEC-Q101 qualified device.

Circuit Diagram



Applications

- EV Fast Charging Modules
- EV On Board Chargers
- Solar Inverters
- Online UPS/Industrial UPS
- SMPS (Switch Mode Power Supplies)
- DC-DC Converters
- ESS (Energy Storage Systems)

Maximum Ratings($T_C = 25^\circ\text{C}$ unless otherwise specified)

| Characteristics | Symbol | Conditions | Min. | Typ. | Max. | Units | Note |
|---------------------------------|----------------|---|------|----------|------|-------|------|
| Drain - Source Voltage | V_{DSmax} | $V_{GS} = 0 \text{ V}, I_D = 100 \mu\text{A}$ | | | 1200 | V | |
| Gate - Source Voltage (dynamic) | V_{GSmax} | AC ($f > 1 \text{ Hz}$) | -8 | | +22 | V | |
| Gate - Source Voltage (static) | V_{GSop} | Static | | -4 / +18 | | V | [1] |
| Continuous Drain Current | I_D | $V_{GS} = 18 \text{ V}, T_C = 25^\circ\text{C}$ | | | 41 | A | |
| | | $V_{GS} = 18 \text{ V}, T_C = 100^\circ\text{C}$ | | | 29 | A | |
| Pulsed Drain Current | $I_{D(pulse)}$ | Pulse width t_p limited by T_{jmax} | | | 123 | A | |
| Power Dissipation | P_D | $T_C = 25^\circ\text{C}$ | | | 306 | W | |
| Avalanche energy | E_{AS} | Single, $V_{DD}=50\text{V}, L=1\text{mH}$ | | | 21 | A | |
| Short - circuit withstand time | t_{sc} | $V_{DD} = 500 \text{ V}, V_{GS} = -4 / 22 \text{ V}, R_G = 20 \Omega, T_J = 25^\circ\text{C}$ | | 3 | | us | |

[1] Recommended turn off gate voltage is -4 V. Recommended turn on gate voltage is 18 V. Do not use with $V_{GSON} < 12 \text{ V}$.

Electrical Characteristics:

| Characteristics | Symbol | Conditions | Min. | Typ. | Max. | Units |
|----------------------------------|---------------|---|------|------|------|------------------|
| Drain Source Breakdown Voltage | $V_{(BR)DSS}$ | $V_{GS} = 0\text{ V}, I_D = 100\ \mu\text{A}$ | 1200 | | | V |
| Gate Threshold Voltage | $V_{GS(th)}$ | $V_{DS} = V_{GS}, I_D = 6\text{ mA}$ | 2 | 2.6 | 4 | V |
| | | $V_{DS} = V_{GS}, I_D = 6\text{ mA}, T_J = 175\text{ }^\circ\text{C}$ | | 1.7 | | V |
| Zero Gate Voltage Drain Current | I_{DSS} | $V_{DS} = 1200\text{ V}, V_{GS} = 0\text{ V}$ | | 1 | 100 | μA |
| Gate Source Leakage Current | I_{GSS} | $V_{GS} = 18\text{ V}, V_{DS} = 0\text{ V}$ | | 10 | 250 | nA |
| Drain Source On-State Resistance | $R_{DS(on)}$ | $V_{GS} = 18\text{ V}, I_D = 15\text{ A}$ | | 80 | 104 | $\text{m}\Omega$ |
| | | $V_{GS} = 18\text{ V}, I_D = 15\text{ A}, T_J = 175\text{ }^\circ\text{C}$ | | 119 | | $\text{m}\Omega$ |
| Transconductance | gfs | $V_{DS} = 20\text{ V}, I_{DS} = 15\text{ A}$ | | 8 | | S |
| | | $V_{DS} = 20\text{ V}, I_{DS} = 15\text{ A}, T_J = 175\text{ }^\circ\text{C}$ | | 9 | | S |
| Input Capacitance | C_{ISS} | $V_{GS} = 0\text{ V}$ | | 984 | | pF |
| Output Capacitance | C_{OSS} | $V_{DS} = 1000\text{ V}$ | | 55 | | |
| Reverse Transfer Capacitance | C_{RSS} | $V_{AC} = 25\text{ mV}$ | | 10 | | |
| COSS Stored Energy | E_{OSS} | $f = 100\text{ KHz}$ | | 33 | | |
| Turn-On Switching Energy | E_{ON} | $V_{DS} = 800\text{ V}, V_{GS} = -4 / 18\text{ V}$ | | 114 | | μJ |
| Turn-Off Switching Energy | E_{OFF} | $I_D = 15\text{ A}, R_{G(ext)} = 0\ \Omega, L = 99\ \mu\text{H}$ | | 2 | | |
| Turn-On Delay Time | $t_{d(on)}$ | $V_{DS} = 800\text{ V}, V_{GS} = -4 / 18\text{ V}$ | | 7 | | ns |
| Rise Time | t_r | $I_D = 15\text{ A}, R_{G(ext)} = 0\ \Omega$ | | 7 | | |
| Turn-Off Delay Time | $t_{d(off)}$ | Inductive Load Timing relative to | | 14 | | |
| Fall Time | t_f | V_{DS} Per IEC60747-8-4 pg 83 | | 12 | | |
| Internal Gate Resistance | $R_{G(int)}$ | $f = 1\text{ MHz}, AC = 25\text{ mV}$ | | 2.1 | | Ω |
| Gate to Source Charge | Q_{gs} | $V_{DS} = 800\text{ V}, V_{GS} = -4 / 18\text{ V}$ | | 11 | | nC |
| Gate to Drain Charge | Q_{gd} | $I_D = 15\text{ A}$ | | 5 | | |
| Total Gate Charge | Q_g | Per IEC60747-8-4 pg 21 | | 53 | | |

Reverse Diode Characteristics (TA = 25 °C, unless otherwise specified)

| Characteristics | Symbol | Conditions | Typ. | Max. | Units |
|----------------------------------|----------|--|------|------|-------|
| Diode Forward Voltage | V_{SD} | $V_{GS} = 0\text{ V}, I_{SD} = 20\text{ A}$ | 1.75 | | V |
| Continuous Diode Forward Current | I_S | $V_{GS} = -4\text{ V}, T_C = 25\text{ °C}$ | 26 | | A |
| Reverse Recovery Time | t_{rr} | $V_{GS} = -4\text{ V}, I_{SD} = 15\text{ A}, T_J = 25\text{ °C}$ | 14 | | ns |
| Reverse Recovery Charge | Q_{rr} | $V_R = 800\text{ V}$ | 190 | | nC |
| Peak Reverse Recovery Current | I_{mm} | $di/dt = 2000\text{ A}/\mu\text{s}$ | 17 | | A |

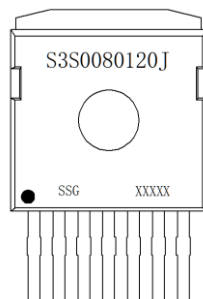
Thermal-Mechanical Specifications

| Characteristics | Symbol | Condition | Specification | Units |
|---|-----------------|--------------|---------------|-------|
| Junction Temperature | T_J | - | -55 to +175 | °C |
| Storage Temperature | T_{stg} | - | -55 to +175 | °C |
| Typical Thermal Resistance Junction to Case | $R_{\theta JC}$ | DC operation | 0.49 | °C /W |

Ordering Information

| Device | Package | Shipping |
|---------------|----------|-------------|
| S3S0080120J | TO-263-7 | 25pcs/tube |
| S3S0080120JTR | TO-263-7 | 800pcs/reel |

Marking Diagram



Where XXXXX is YYWWL

- S3S = Device Type
- 0080 = RDS(on)
- 120 = Reverse Voltage (1200V)
- J = Package
- SSG = SSG
- YY = Year
- WW = Week
- L = Lot Number

Cautions: Molding resin
Epoxy resin UL94V-0

Ratings and Characteristics Curves

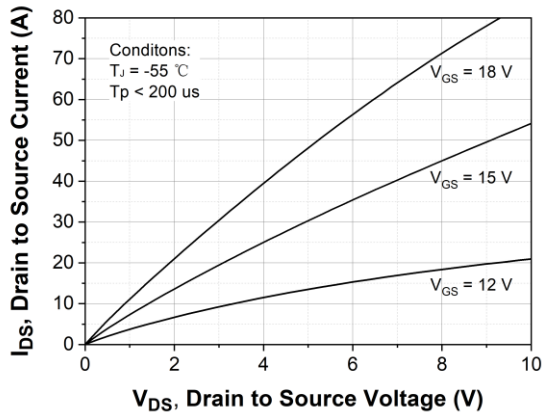


Figure 1. Output Characteristics $T_J = -55\text{ }^\circ\text{C}$

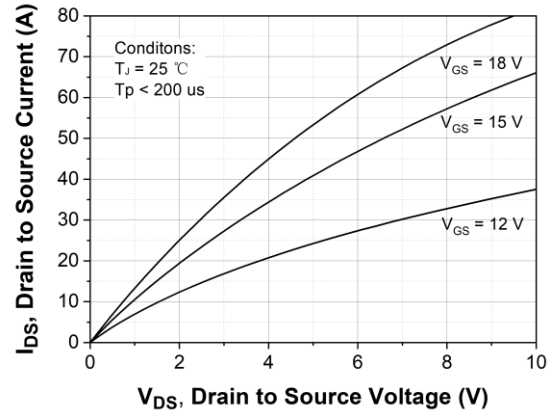


Figure 2. Output Characteristics $T_J = 25\text{ }^\circ\text{C}$

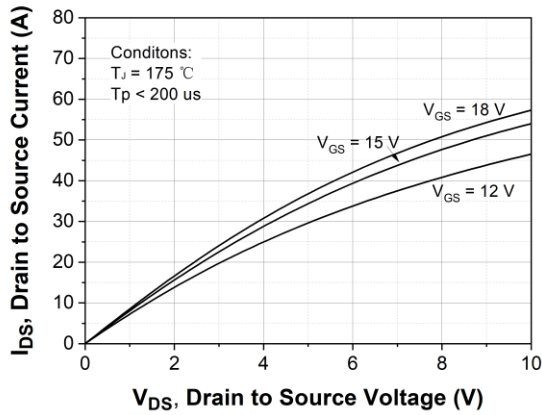


Figure 3. Output Characteristics $T_J = 175\text{ }^\circ\text{C}$

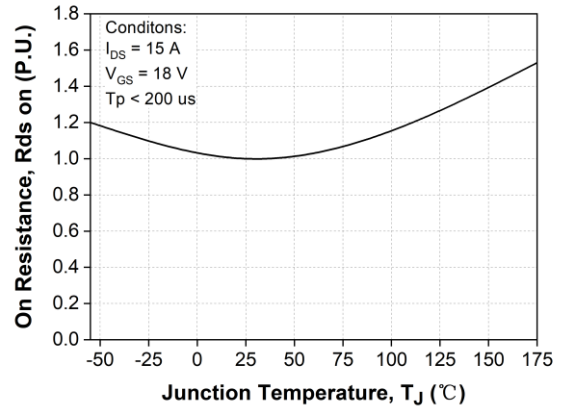


Figure 4. Normalized On-Resistance vs. Temperature

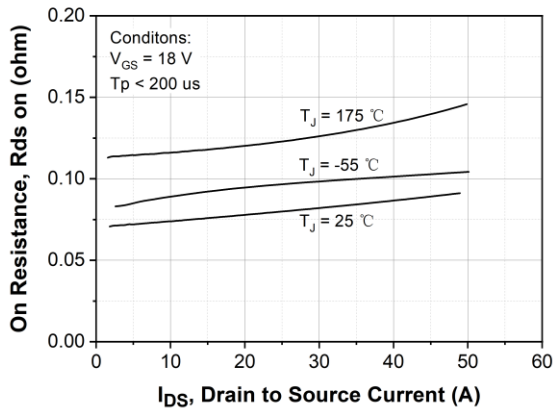


Figure 5. On-Resistance vs. Drain Current For Various Temperatures

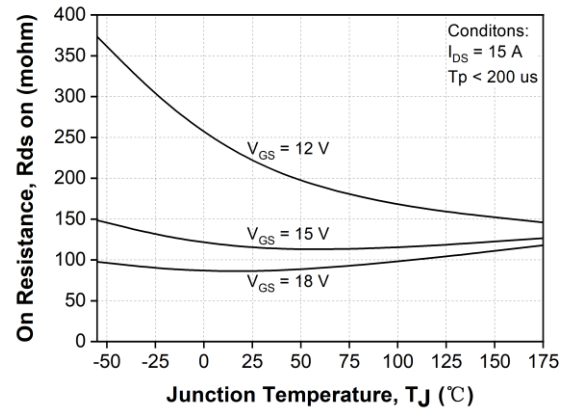


Figure 6. On-Resistance vs. Temperature For Various Gate Voltage

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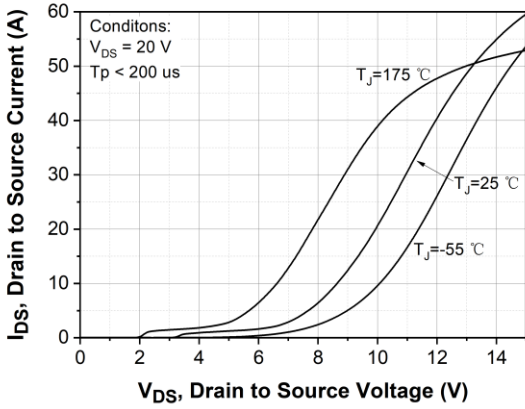


Figure 7. Transfer Characteristic for Various Junction Temperatures

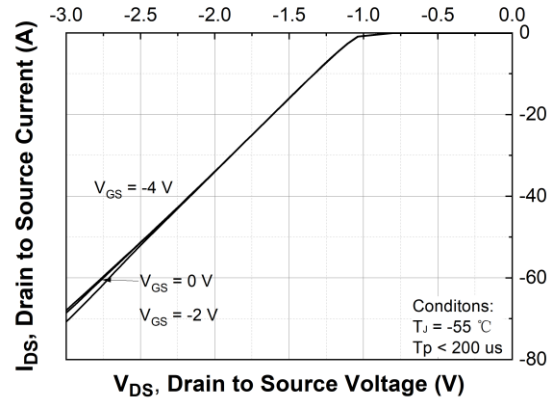


Figure 8. Body Diode Characteristic at $T_J = -55^\circ\text{C}$

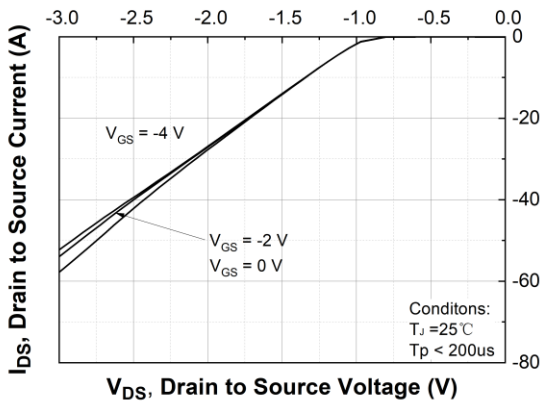


Figure 9. Body Diode Characteristic at $T_J = 25^\circ\text{C}$

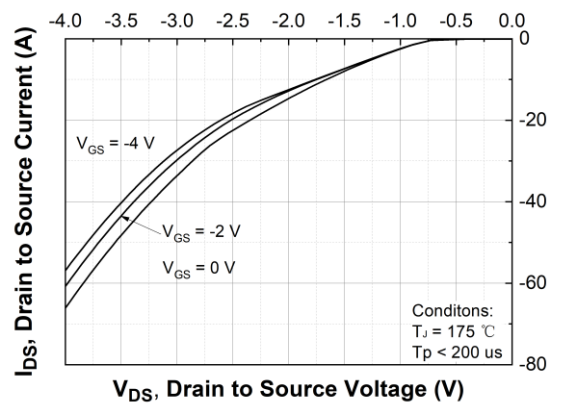


Figure 10. Body Diode Characteristic at $T_J = 175^\circ\text{C}$

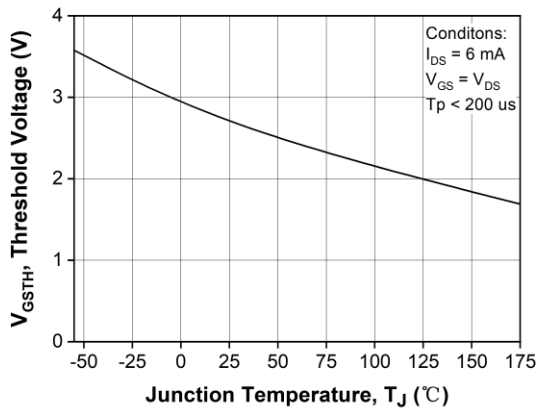


Figure 11. Threshold Voltage vs. Temperature

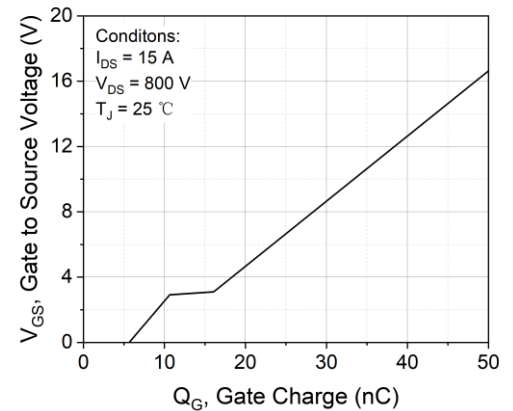


Figure 12. Gate Charge Characteristic

Technical Data
Data Sheet N3176, Rev.-

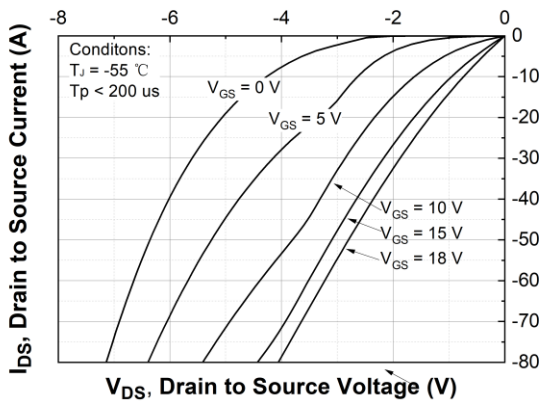


Figure 13. 3rd Quadrant Characteristic at $T_j = -55$ °C

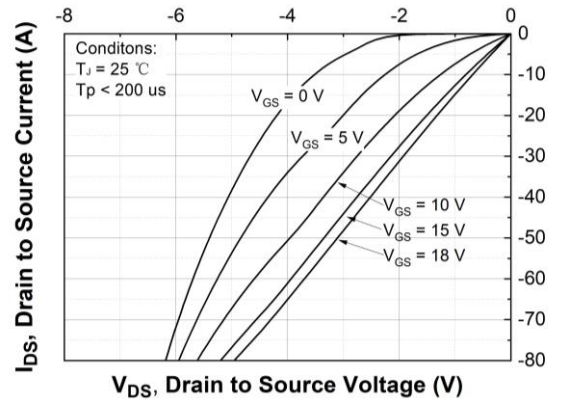


Figure 14. 3rd Quadrant Characteristic at $T_j = 25$ °C

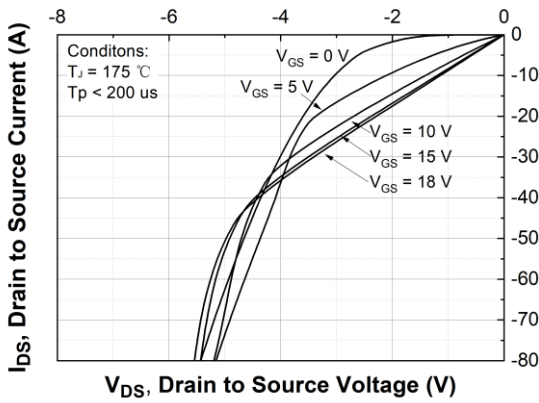


Figure 15. 3rd Quadrant Characteristic at $T_j = 175$ °C

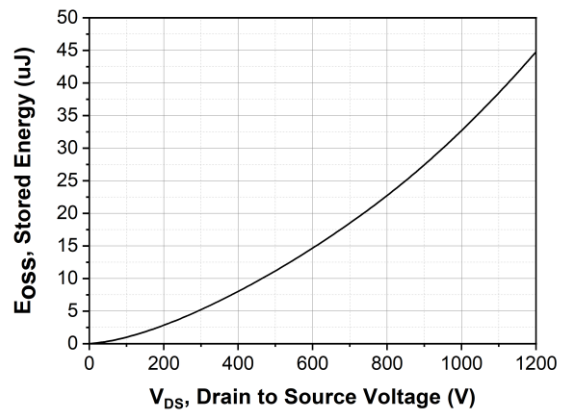


Figure 16. Output Capacitor Stored Energy

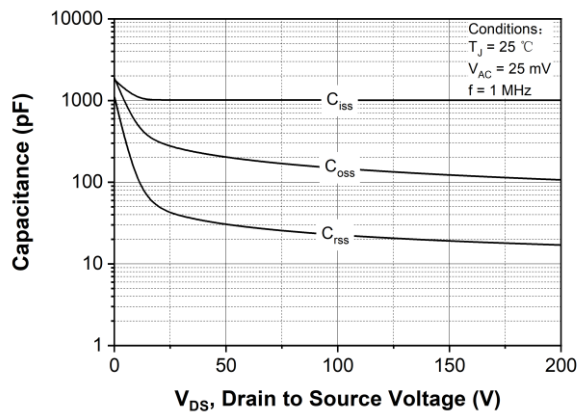


Figure 17. Capacitances vs. Drain-Source Voltage (0 - 200 V)

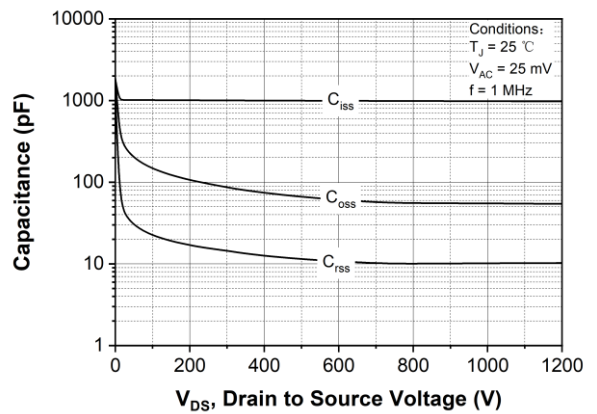


Figure 18. Capacitances vs. Drain-Source Voltage (0 - 1200 V)

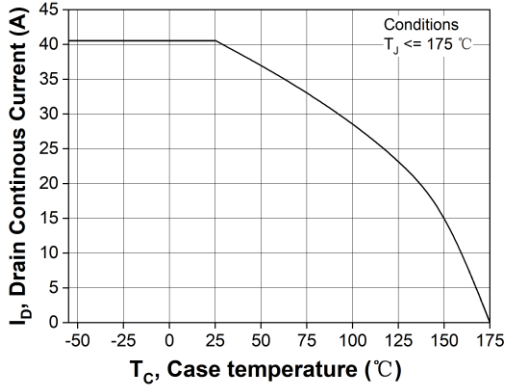


Figure 19. Continuous Drain Current Derating vs. Case Temperature

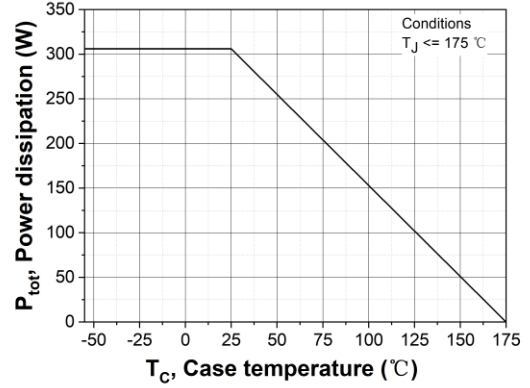


Figure 20. Maximum Power Dissipation Derating vs. Case Temperature

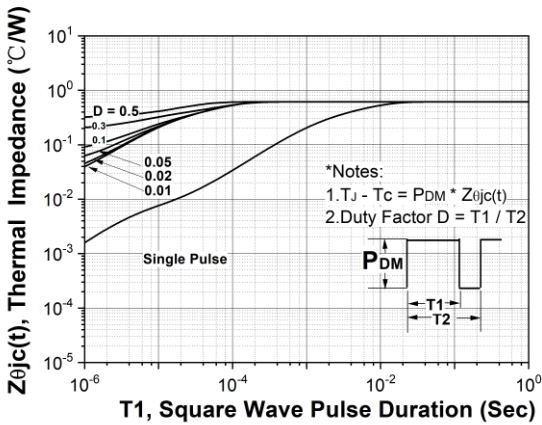


Figure 21. Transient Thermal Impedance (Junction - Case)

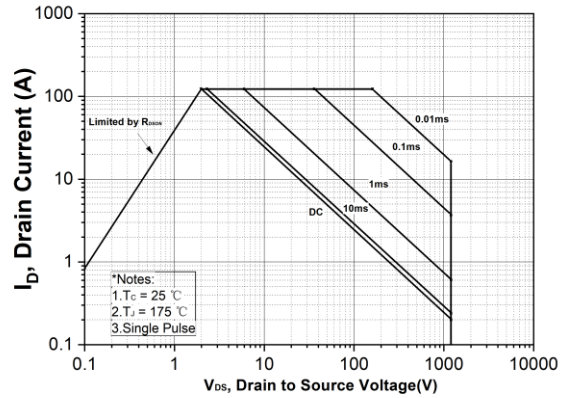


Figure 22. Safe Operating Area

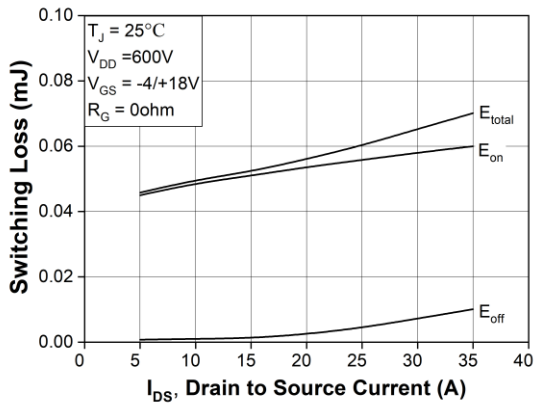


Figure 23. Clamped Inductive Switching Energy vs. Drain Current ($V_{DD} = 600V$)

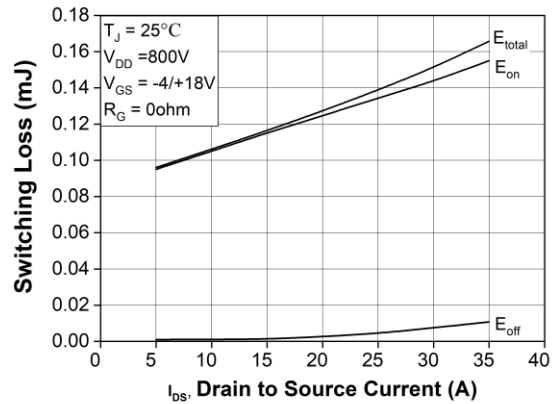


Figure 24. Clamped Inductive Switching Energy vs. Drain Current ($V_{DD} = 800V$)

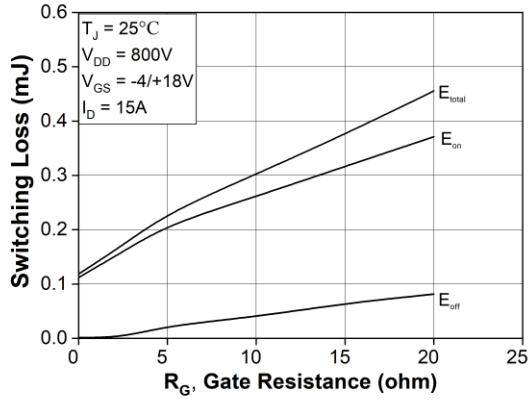


Figure 25. Clamped Inductive Switching Energy vs. $R_{G(ext)}$

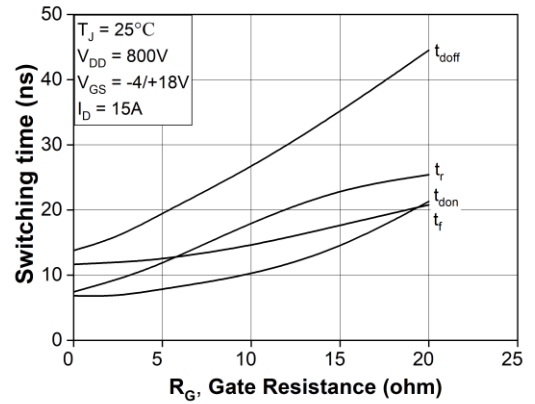


Figure 26. Switching Times vs. $R_{G(ext)}$

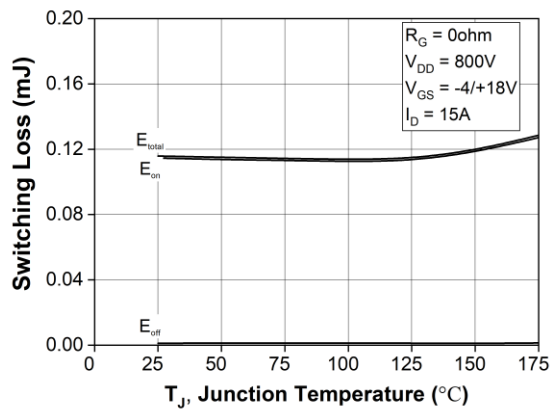
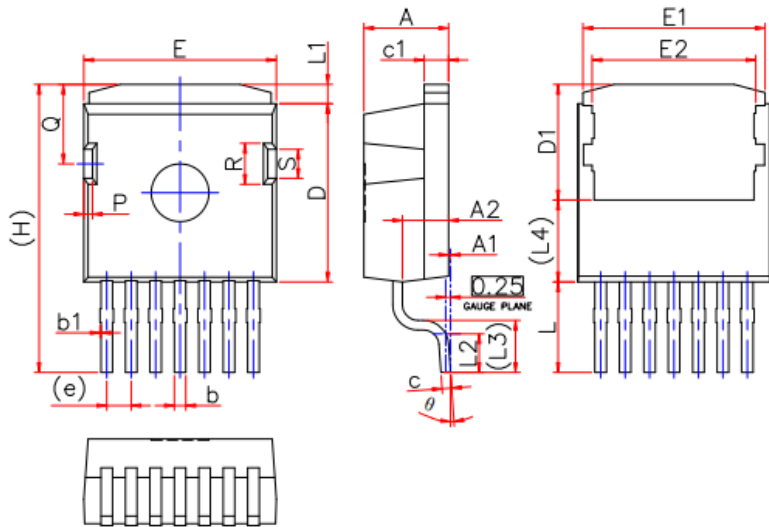


Figure 27. Clamped Inductive Switching Energy vs. Temperature

Mechanical Dimensions TO-263-7



| SYMBOL | Millimeters | | |
|--------|-------------|--------|--------|
| | MIN. | TYP. | MAX. |
| A | 4.300 | 4.400 | 4.500 |
| A1 | 0.000 | 0.100 | 0.200 |
| A2 | 2.300 | 2.400 | 2.500 |
| b | 0.500 | 0.600 | 0.700 |
| b1 | 0.000 | 0.075 | 0.150 |
| c | 0.400 | 0.500 | 0.5600 |
| c1 | 1.170 | 1.270 | 1.370 |
| D | 9.050 | 9.250 | 9.450 |
| D1 | 5.900 | 6.000 | 6.100 |
| E | 9.800 | 10.000 | 10.200 |
| E1 | 9.360 | 9.460 | 9.560 |
| E2 | 8.400 | 8.500 | 8.600 |
| e | 1.270 REF | | |
| H | 15.000 REF | | |
| L | 4.200 | 4.700 | 5.200 |
| L1 | 0.700 | 1.000 | 1.300 |
| L2 | 1.700 | 2.000 | 2.300 |
| L3 | 2.700 REF | | |
| L4 | 4.250 REF | | |
| P | 0.350 | 0.450 | 0.550 |
| Q | 4.020 | 4.120 | 4.220 |
| R | 2.030 | 2.130 | 2.230 |
| S | 1.400 | 1.500 | 1.600 |
| θ | 0° | 4° | 8° |

The outline from different package houses may have slight differences. So the outline above is just schematic. The dimensions are controlled per specifications.

Technical Data
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